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|-----|------|--|-----------------|----|----|------------------|
| S55 | 65 | ((frequency adj divider) with (reference adj frequency) with (PLL phase adj lock adj loop)) and "455"/\$.ccls. and @ad<="20010316" | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:20 |
| S56 | 165 | filter with (voltage near2 driven) | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:23 |
| S57 | 26 | (filter with (voltage near2 driven)) and (pll phase adj lock adj loop) | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:22 |
| S58 | 68 | filter same (voltage adj driven) | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:30 |
| S59 | 62 | (filter same (voltage adj driven)) and @ad<="20010316" | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:25 |
| S60 | 33 | filter with (voltage adj driven) | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:30 |
| S61 | 28 | (filter with (voltage adj driven)) and @ad<="20010315" | US-PGPUB; USPAT | OR | ON | 2004/04/28 17:30 |
| S62 | 221 | 340/856.4,582,7.6.ccls. | US-PGPUB; USPAT | OR | ON | 2004/04/29 09:43 |
| S63 | 212 | 340/856.4,582,7.6.ccls. | USPAT | OR | ON | 2004/04/29 13:29 |
| S64 | 3271 | 331/1R,177R;375/376;455/192.1, 192.2,255-260,264,265.ccls. and @ad<="20010316" | US-PGPUB; USPAT | OR | ON | 2004/12/15 14:53 |
| S65 | 271 | 331/1R,177R;375/376;455/192.1, 192.2,255-260,264,265.ccls. and @ad<="20010316" and (PLL near4 synthesizer) | US-PGPUB; USPAT | OR | ON | 2004/12/15 14:54 |
| S66 | 271 | 331/1R,177R;375/376;455/192.1, 192.2,255-260,264,265.ccls. and @ad<="20010316" and (PLL near4 synthesizer) | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:47 |
| S67 | 16 | (feedback control) same (period\$8 near3 var\$3) same (phase near3 comparat\$3) | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:11 |
| S68 | 34 | (period\$8 near3 var\$3) same (phase near3 comparat\$3) | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:13 |
| S69 | 406 | (period\$8 near3 var\$3) same comparat\$3 | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:47 |
| S72 | 228 | (period\$5 near4 (var\$5 chang\$3)) same (phase near3 comparat\$3) | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:19 |
| S73 | 6 | S66 and S72 | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:20 |
| S74 | 15 | S72 and @ad<="20010316" and (PLL near4 synthesizer) | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:24 |
| S75 | 0 | S66 and S69 | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:46 |

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|-----|-------|---|--------------------|----|----|------------------|
| S76 | 10089 | (period\$8 near4 (var\$5 chang\$5)) same compar\$5 | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:50 |
| S77 | 10 | S66 and S76 | US-PGPUB; USPAT | OR | ON | 2004/12/16 09:51 |